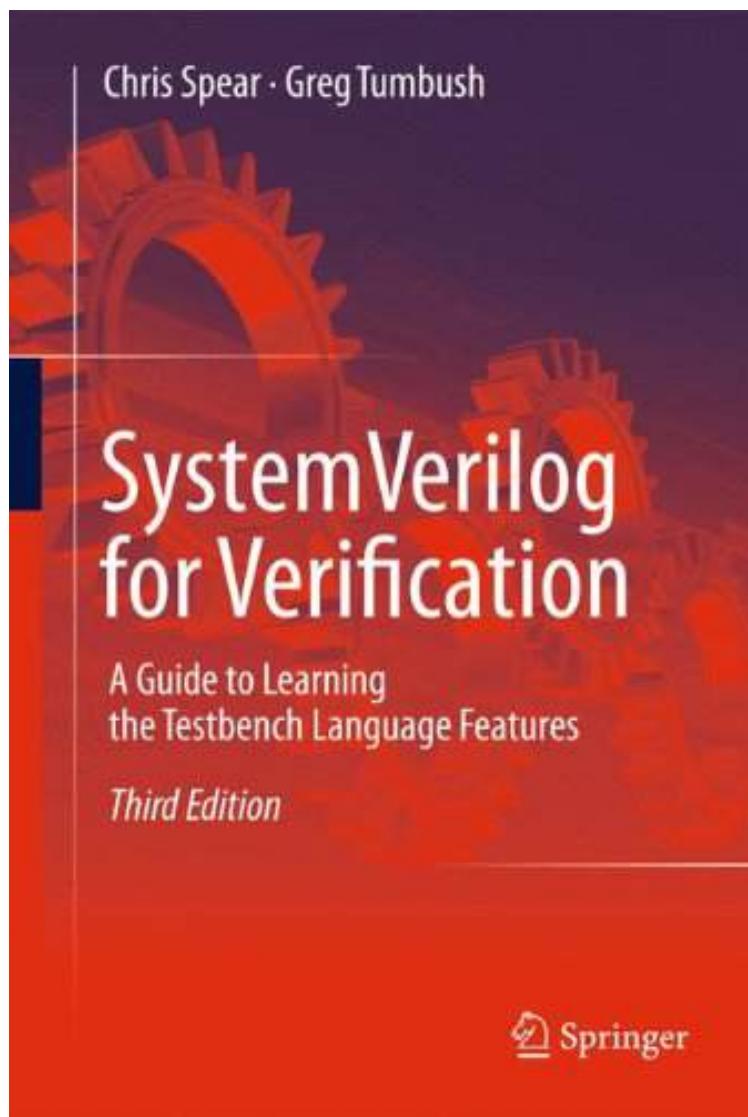


SystemVerilog for Verification: A Guide to Learning the Testbench Language Features

By Chris Spear, Greg Tumbush

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